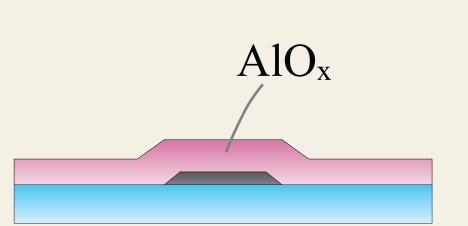
- Fabrication process -

(1) Fabrication of gate electrode



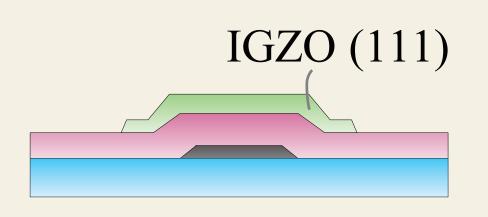
sputtering, 50 nm 150°C Patterning: Wet etching

(2) Fabrication of gate insulator



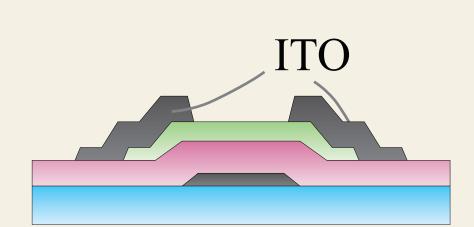
mist CVD (1 atm) 430° C, $\approx 100 \text{ nm}$ Patterning: Wet etching

(3) Fabrication of channel layer



mist CVD (1 atm) 350° C, $\approx 50 \text{ nm}$ Patterning: Wet etching

(4) Fabrication of source & drain electrode

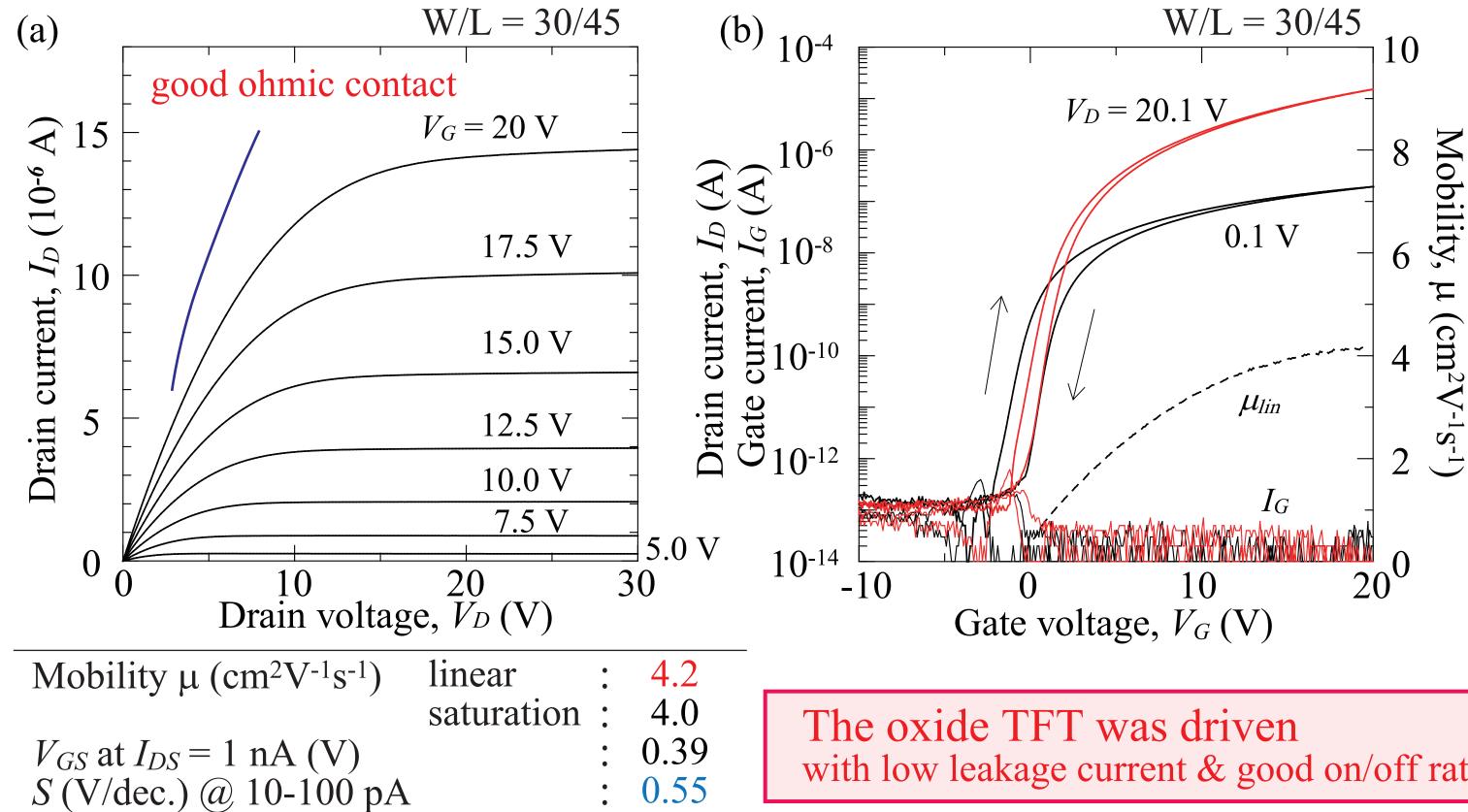


sputtering, 50 nm, RT Patterning: Wet etching (lift-off)

(5) Post annealing

 $H_2(5\%) + N_2$, 350°C, 1 h

- The drive of the first prototype -



with low leakage current & good on/off ratio. 0.68 Hysteresis $\Delta V_H(V)$

 $S \& \Delta V_H$ are worse than previous reports $: > 10^8$ I_{on}/I_{off} at $V_{GS} = 30/-10 \text{ V}$: <10-12 Gate leakage current (a), $V_G = 20$ V

In this experiment, for the system construction of mist CVD, TFT was left under atmosphere for several hours after growing AlO_x insulator before growing IGZO channel layer. Therefore, the creation of defects or traps in the IGZO channel layer or in interface between the AlOx insulator and

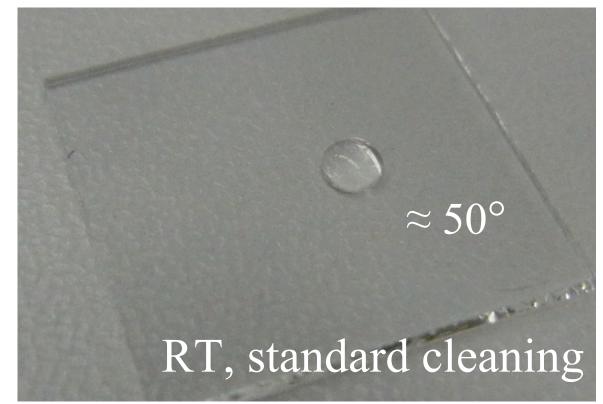
(a) Output and (b) transfer characteristics of oxide TFT with a IGZO/AlOx stack fabricated by mist CVD.

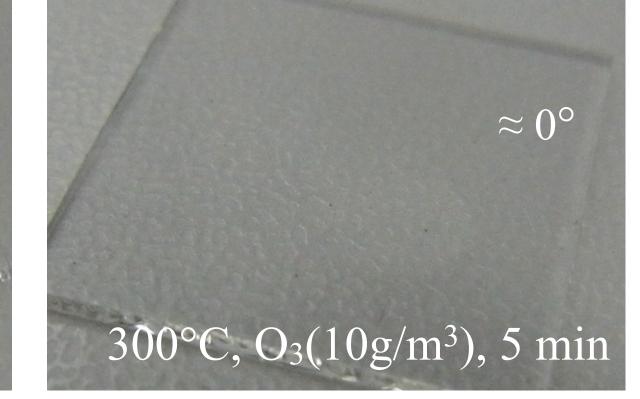
the IGZO channel layer is not denied. Moreover, in this research, the oxide TFT has no passivation film.

 $S \& \Delta V_H$ may be improved with continuous process and interface treatment.

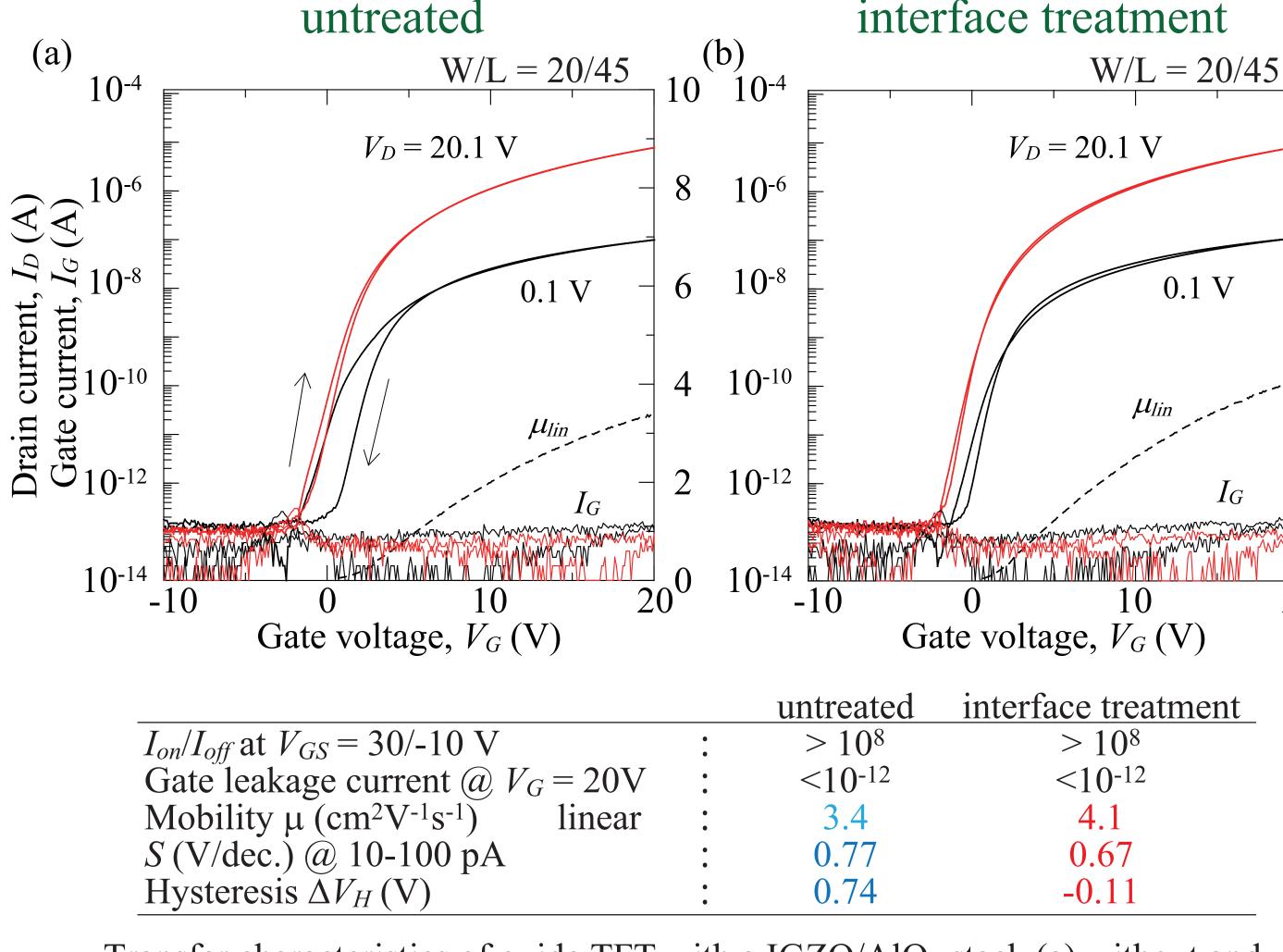
- Effect of O₃ treatment on the AlO_x -

Treatment process of the AlO_x thin film surface was added for 5 min at 300°C under O₃ ambient, after growth of AlO_x and just before growth of IGZO. After the O₃ treatment, the surface of AlO_x thin films was cleaned with eliminating of impurities, such as an organic matter adhering to the surface and was changed to the hydrophillic.

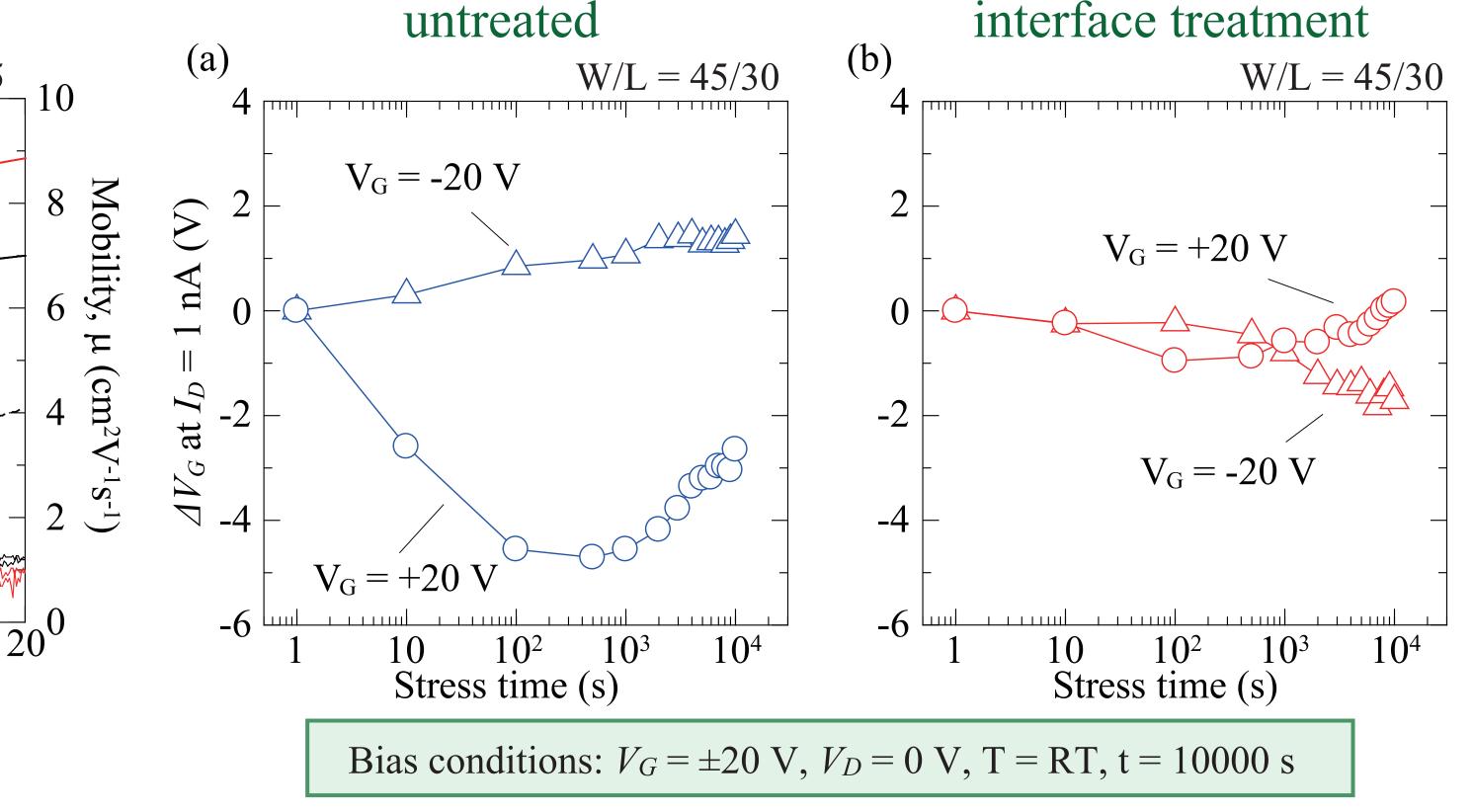




- Effect of O₃ treatment on the interface between AlO_x & IGZO -



Transfer characteristics of oxide TFT with a IGZO/AlO_x stack (a) without and (b) with interface treatment.



Life time of oxide TFT with a IGZO/AlOx stack (a) without and (b) with interface treatment.

The improvement of μ and ΔH was found with the O₃ treatment. The effect of the O_3 treatment is clearly seen in the life time evaluation.

S is still worse than previous reports.

Conclusion

- 1) Oxide TFT consisting of both channel layer (IGZO) and gate insulator (AlO_x) grown by mist CVD which is one of suitable techniques for growing thin films continuously under atmosphere was fabricated.
- The improvement of μ and ΔH was seen with introduction of the interface treatment under O₃ ambient.
- 3) The index of non-vacuum process conversion of the TFT fabrication process was demonstrated with fabricating the oxide TFT with an IGZO/AlO_x stack grown by the mist CVD.

Please refer

"Electrical Properties of the Thin-Film Transistor With an Indium—Gallium—Zinc Oxide Channel and an Aluminium Oxide Gate Dielectric Stack Formed by Solution-Based Atmospheric **Pressure Deposition**"

M. Furuta, T. Kawaharamura, D. Wang, T. Toda, and T. Hirao, IEEE Electron Device Lett. Vol.33 (2012) pp.851-853.

Contact me!

Affiliation: Institute for Nanotechnology, **Kochi University of Technology** 185 Miyanokuti Tosayamada-chou **Address:**

Kami-city, Kochi, 782-8502, Japan +81-887-57-2747, +81-887-57-2714 Tel., Fax.: kawaharamura.toshiyuki@kochi-tech.ac.jp E-mail: Web page

http://www.nano.kochi-tech.ac.jp/tosiyuki/index.html